

Notice of References Cited	Application/Control No. 09/823,276	Applicant(s)/Patent Under Reexamination INOUE ET AL.	
	Examiner Barry J. O'Brien	Art Unit 2183	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,155,817	10-1992	Kishigami et al.	712/218
	B	US-5,915,110	06-1999	Witt et al.	712/239
	C	US-5,872,951	02-1999	Tran, Thang M.	712/218
	D	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ozer et al., A Fast Interrupt Handling Scheme for VLIW Processors, Proceedings of the International Conference on Parallel Architectures and Compilation Techniques, Oct. 1998, p.136-141
	V	Smith, et al., Implementing Precise Interrupts in Pipelined Processors, IEEE Transactions on Computers, May 1988, p.562-573
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.